

remains the assignee of U.S. Patent No. 5,590,342, as established by the assignment to Intel Corporation recorded at the Patent Office at reel 7341, frames 0694 and 0695 on January 24, 1995.

IN THE TITLE

Please change the title to "Method And Apparatus For Adjusting A Power Consumption Level Based On The Amount Of Time A Processor Clock Is Stopped".

IN THE SPECIFICATION

Please insert as the first sentence of the specification, "This is a continuation of application Serial No. 09/224,620 filed on December 31, 1998."

IN THE CLAIMS

Please cancel claims 1-35.

Please cancel claims 36-50 (to the extent they would be present in this application).

Please add the following Claims.

- 36
51. ~~A machine-readable medium having stored thereon instructions, which if~~
executed by a machine, cause said machine to perform operations comprising:
determining an amount of time a processor is in a first power
consumption state, said amount of time said processor is in said
first power consumption state comprising a period of time in which
a clock of said processor is stopped;
reducing a voltage level applied to said processor in response to said

~~amount of time said processor is in said first power consumption~~
~~state.~~

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The machine-readable medium of Claim 51 wherein reducing said voltage level applied to said processor is performed in response to said amount of time exceeding a selected amount of time.

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The machine-readable medium of Claim 52 wherein determining comprises reading a timer to determine said amount of time said processor is in said first power consumption state.

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The machine-readable medium of Claim 53 wherein said reducing said voltage level comprises placing said processor in a power-off state.

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The machine-readable medium of Claim 51 wherein determining the amount of time said processor is in the first power consumption state comprises monitoring an activity level demand within a computer system.

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The machine-readable medium of Claim 55 wherein reducing the voltage level applied to the processor comprises operating said processor in a reduced power consumption state while satisfying said activity level demand.

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A machine-readable medium having stored thereon instructions, which if
executed by a machine, cause said machine to perform operations comprising:
determining an amount of time a clock of a processor is stopped;
placing said processor into a reduced power consumption state in
response to said amount of time said clock of said processor is stopped.

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The machine-readable medium of Claim 57 wherein said placing comprises
reducing a voltage of said processor.

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The machine-readable medium of Claim 58 wherein said determining an amount
of time said clock of said processor is stopped contributes to determining
a system idle time.

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The machine-readable medium of Claim 59 wherein said clock of said processor
is stopped when the processor is in a sleep state.

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The machine-readable medium of Claim 60 wherein a timer is read to
determine said amount of time said processor is in said first power
consumption state.

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The machine-readable medium of Claim 61 wherein said reduced power
consumption state is a power-off state.

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~~The machine-readable medium of Claim 58 wherein reducing said voltage of~~
~~said processor is performed in response to said amount of time said clock~~
~~of said processor is stopped exceeding a selected amount of time.~~

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An apparatus comprising:

a power management module to determine an amount of time a processor
is in a first power consumption state, said first power consumption
state comprising a period in which said processor is stopped;
a power reduction module to place said processor into a reduced power
consumption state in response to said amount of time said
processor is in said first power consumption state.

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The apparatus of Claim 49 wherein said power reduction module comprises a
software routine.

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The apparatus of Claim 49 wherein said power management module
comprises a timer.

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The apparatus of Claim 50 wherein said power management module
comprises a timer.

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68. ⁵²
~~The apparatus of Claim 67 wherein said power reduction module is enabled to~~
reduce a voltage applied to said processor.

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69. ⁵³
~~The apparatus of Claim 68 wherein said power reduction module is enabled to~~
reduce said voltage in response to said amount of time exceeding a
selected amount of time.

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70. ⁵⁴
~~The apparatus of Claim 69 wherein said power management module comprises a~~
software routine.

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71. ⁵⁵
~~The apparatus of Claim 70 wherein said first power consumption state is a sleep~~
state.

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72. ⁵⁶
~~The apparatus of Claim 71 wherein said reduced power consumption state is a~~
power-off state.

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An apparatus comprising:
a power management module to determine an amount of time a processor
is in a first power consumption state, said first power consumption
state comprising a period in which said processor is stopped;
a power reduction module to place said processor into a reduced power

~~consumption state in response to said amount of time said~~
processor is in said first power consumption state, said power
reduction module comprising a software routine, said power
management module comprising a timer.

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A system comprising:

a memory;

a processor coupled to said memory;

a power management module to detect an amount of time said processor
is in a first power consumption state, said first power consumption
state comprising a period of time in which a clock of said processor
is stopped, said processor being placed into a reduced power
consumption state in response to said amount of time said
processor is in said first power consumption state.

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The system of Claim 74 wherein said reduced power consumption state
comprises a reduced voltage state of said processor.

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The system of Claim 75 wherein said reduced voltage state comprises a power-
off state.

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77. ⁶¹ ~~The system of Claim 76 wherein said power management module is enabled to~~
determine a system idle time.

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78. ⁶² The system of Claim 77 wherein said system idle time is represented by said
amount of time said processor is in said first power consumption state.

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79. ⁶³ The system of Claim 78 wherein said first power consumption state is a sleep
state.

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80. ⁶⁴ The system of Claim 79 wherein said power management module comprises a
software routine.

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81. ⁵⁹ The system of Claim 74 wherein said power management module comprises a
software routine.

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82. ⁵⁹ The system of Claim 74 wherein said power management module further
comprises a timer.

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83. ⁶⁶ The system of Claim 81 wherein said power management module further
comprises a timer.

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84. ⁵⁹ The system of Claim 74 further comprising
a configurable device;

~~power management software to power manage said configurable device.~~

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The system of claim 84 further comprising:

power management software to cooperate with said device manager to allow power management of a plurality of devices in the system which are configurable devices, and to manage a power level for each of the plurality of devices in the system. the power management software being capable of placing one or more of said plurality of devices in a reduced power consumption state.

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The system of claim 85 further comprising a plug and play manager.

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The system of claim 86 wherein said power management software is to

communicate with said plug and play manager to update data structures if configuration changes occur to allow power management of dynamically reconfigurable devices.

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The system of claim 87 wherein said power management software registers with said device manager to be notified of configuration changes.

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The system of claim 88 wherein said power management software is to provide

~~system level power management including the use of multiple system~~
level power management states for said system, and to provide multiple
power management states for said plurality of devices.

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90. ⁷³ The system of claim 88 wherein said power management software is to provide
support for idle detection for at least one of said plurality of devices.

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91. ⁷³ The system of claim 88 wherein said power management software is to place the
system in a sleep state when the system is idle and to keep said system in
said sleep state until activity is detected, and wherein the sleep state is one
of a plurality of system power management states, and further wherein
said system stops a clock for a system processor in said sleep state.

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92. A method comprising:
determining an amount of time a clock of a processor is stopped;
placing said processor into a reduced power consumption state in
response to said amount of time said clock of said processor is
stopped.

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93. ⁷⁷ The method of Claim 92 wherein said placing comprises reducing a voltage of
said processor.

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The method of Claim 93 wherein said determining said amount of time said clock of said processor is stopped contributes to determining a system idle time.

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The method of Claim 94 wherein said clock of said processor is stopped when the processor is in a sleep state.

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The method of Claim 95 wherein a timer is read to determine said amount of time said processor is in a first power consumption state.

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The method of Claim 96 wherein said reduced power consumption state is a power-off state.

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The method of Claim 97 wherein reducing said voltage of said processor is performed in response to said amount of time said clock of said processor is stopped exceeding a selected amount of time.

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The method of claim 98 further comprising power managing in cooperation with a device manager a plurality of devices in a system which are configurable devices; and managing a power level for each of the plurality of devices in the system, the power managing comprising placing one or more of said plurality of devices in a reduced power consumption state.

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100. ~~The method of claim 99 wherein said system comprises a plug and play~~
manager.

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101. ~~The method of claim 100 wherein said power managing comprises~~
communicating with said plug and play manager to update data
structures if configuration changes occur to allow power management of
dynamically reconfigurable devices.

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102. ~~The method of claim 101 further comprising registering power management~~
software with said device manager to be notified of configuration
changes.

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103. ~~The system of claim 102 wherein said power managing comprises providing~~
system level power management including the use of multiple system
level power management states for said system, and to provide multiple
power management states for said plurality of devices.

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104. ~~The method of claim 102 wherein said power managing comprises supporting~~
idle detection for at least one of said plurality of devices.

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105. ~~The method of claim 102 wherein said power managing comprises placing the~~

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system in a sleep state when the system is idle and to keep said system in
said sleep state until activity is detected, and wherein the sleep state is one
of a plurality of system power management states, and further wherein
said system stops a clock for a system processor in said sleep state.

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